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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

Application Number	09/750,629
Filing Date	12/28/2000
First Named Inventor	Wang
Group Art Unit	2182
Examiner Name	unknown
Attorney Docket Number	00CXT0785N

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
wh	2	MU, A.; LARSON, J.; "A 9.6 gigaByte/s Throughput Plesiochronous Routing Chip" 1996, 261-266, HAL Computer Systems, Inc., Campbell, CA 95008	
wh	3	NAKANO, Y., "VLSI for Digital Cross-Connect Switch IN Broad-Band Access Network," 1987, Hitachi Ltd. Kokubunji, Tokyo, Japan	

Examiner
Signature

Wim T. Huynh

Date
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5/31/03

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(use as many sheets as necessary)

Sheet	1	of	1
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Complete if Known

Application Number	Not yet assigned
Filing Date	Herewith
First Named Inventor	Yuanlong Wang
Group Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	00CXT0785N

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[illegible]

Examiner
Signature

him T. Hynh

Date
Considered

5/31/03

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